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Docket No. 252069US2

IN RE APPLICATION OF: Hideki TAKAHASHI, et al.

SERIAL NO: 10/826,391

FILED: April 19, 2004

FOR: INSULATED GATE BIPOLAR TRANSISTOR WITH BUILT-IN FREEWHEELING DIODE

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Transmitted herewith is an amendment in the above-identified application.

- ☒ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☐ Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	16	MINUS	20	0	x \$50 =	\$0.00
INDEPENDENT	3	MINUS	3	0	x \$200 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$360 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment			+ \$40 =	\$0.00
		TOTAL				\$0.00

- ☐ A check in the amount of **\$0.00** is attached.
- ☐ Credit card payment form is attached to cover the fees in the amount of **\$0.00**
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters  
Registration No. 28,870

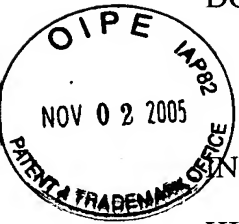
Customer Number

**22850**

Tel. (703) 413-3000  
Fax. (703) 413-2220  
(OSMMN 05/03)

Zachary S. Stern  
Registration No. 54,719

DOCKET NO: 252069US2



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :

HIDEKI TAKAHASHI, ET AL. :

EXAMINER: LANDAU, M. C.

SERIAL NO: 10/826,391 :

FILED: APRIL 19, 2004 :

GROUP ART UNIT: 2815

FOR: INSULATED GATE BIPOLAR  
TRANSISTOR WITH BUILT-IN  
FREEWHEELING DIODE :

AMENDMENT

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated August 2, 2005, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 9 of this paper.